NARAYANA ENGINEERING COLLEGE::NELLORE
AUTONOMOUS

A.K.NAGAR, NELLORE-524004, A.P, INDIA

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 16/06/2022

Report on Virtual Seminar on
"Job Opportunities & Skill Set for Verilog HDL Design & Verification"

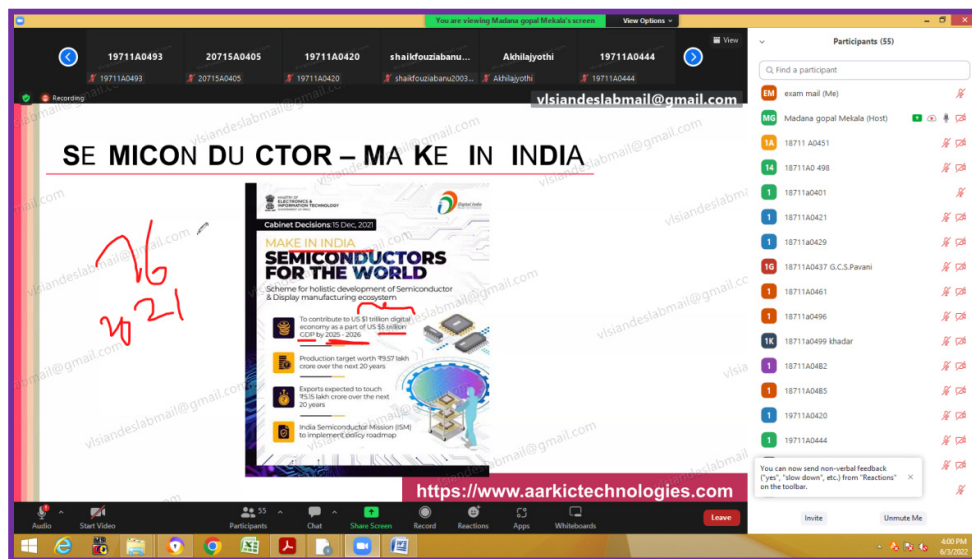| 1	Name of the Activity/Event	Virtual Seminar on **"Job Opportunities & Skill Set for Verilog HDL Design & Verification"**		
2	Date of Activity/Event	**03/06/2022**		
3	Organized by/Name of the committee	Dept. of Electronics & Communication Engineering		
4	Place of Activity/event	Narayana Engineering College, Nellore		
5	Resource person/guest/organization	Mr. Madan Gopal M, Corporate Training & Placement Consultants, Aark IC Technologies, Bangalore		
6	Type of activity/Event	Virtual Seminar(IIIC)		
7	Activity/Event Objectives	1. To understand various opportunities after B.Tech 2. To make their decision easier after their engineering studies. 3. To obtain a challenging position in a high quality engineering environment.		
8	Participation	Students	Faculty	Total
		55	-	55
9	General remarks	The sessions are helpful to the students about selecting the right path after B.Tech.		
10	Suggested Improvements	It would be useful if there is face to face interaction.		
11	Enclosures	1. Request letter 2. Circulars 3. Report 4. Attendance		
12	Signature of In charge/convener			

A BRIEF DESCRIPTION OF THE EVENT:

Department of Electronics and Communication engineering has conducted a virtual seminar on **"**Job Opportunities & Skill Set for Verilog HDL Design & Verification" under IIIC for IV B-Tech ECE students on 03-06-2022. The Resource Person for the event is by Mr. Madan Gopal M, Corporate Training & Placement Consultants, Aark IC Technologies, Bangalore.

The Head of the Department Dr. K. Murali introduced the resource person. The Resource person started her lecture by stating that Engineering is one of the brightest and evergreen profession

which has a great demand. Every year lot of students try hard to crack the entrance exams and select the engineering stream which they have an interest in. A degree in B.Tech (also known as engineering degrees) is one of the most popular fields of education these days.

There are few myths that are common regarding engineering. Some of the myths of engineering you should know are,
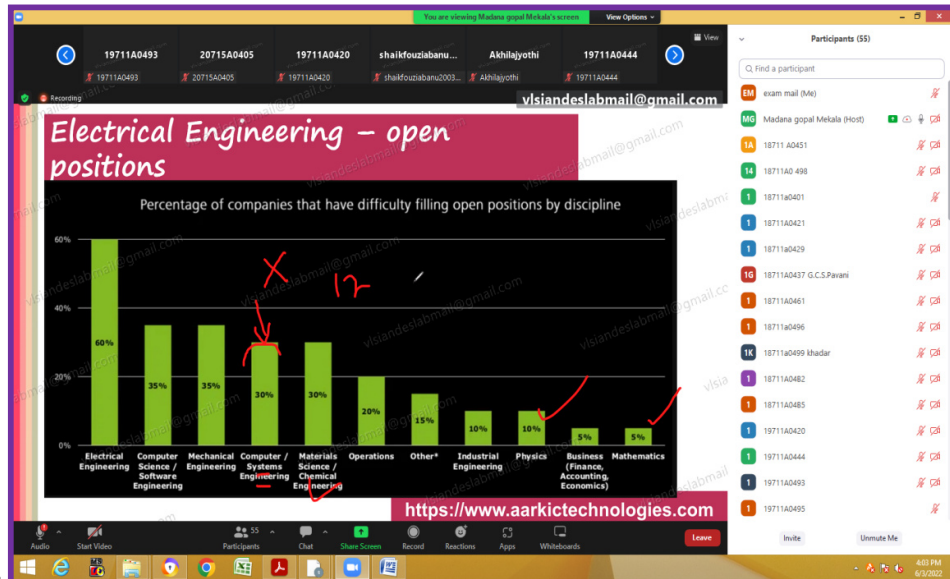
➢ Engineers have super-powers

➢ Engineering is more valuable than science

➢ Requirement of more IITs

➢ A person having B.E/B. Tech graduate is an "Engineer"

➢ Management degree along with engineering is a great compilation.

➢ R&D engineering is best done by foreign MNCs.

Resource person explaining overview of career opportunities in VLSI Industry after B.Tech.

Resource person explaining overview of career opportunities in VLSI Industry after B.Tech

Resource Person discussing the opportunity in Electrical Engineering

Resource Person discussing the opportunity in VLSI Industry

Resource person has explained the topics in Verilog HDL Design and Verification are

- ❖ Verilog HDL Modeling
- ❖ Verilog RTL Design Model
- ❖ Design and Verification Methods
- ❖ RTL Design Verification Skill
- ❖ Design Example - 4 bit Counter

At the end Dr. K. Murali, HOD of ECE expressed gratitude to the resource person for giving valuable suggestions regarding various career opportunities that are available after B.Tech.